Claims

- [1]
- 1. An electronic apparatus comprising a conductive layer supported by a substrate and a metal layer of a material having a resistivity lower than that of the conductive layer, the metal layer being extended on the conductive layer, the conductive layer having an oxidation resistivity higher than that of the metal layer and forming a terminal for connecting to peripheral circuitry, wherein: the metal layer extends on an extending portion of the conductive layer outside the terminal of the conductive layer, and/or on the periphery of or in the vicinity of a coupling area for making the conductive layer to be exposed to the exterior within an area of the terminal of the conductive layer; and there is provided an electrically insulating layer which covers at least a part of the terminal of the conductive layer and the whole of the metal layer and which extends on the area other than the coupling area within the area of the terminal of the conductive layer.

2. An electronic apparatus comprising a conductive layer supported by a

[2]

substrate and a metal layer of a material having a resistivity lower than that of the conductive layer, the metal layer being extended on the conductive layer, the conductive layer having an oxidation resistivity higher than that of the metal layer and forming a terminal for connecting to peripheral circuitry, wherein: the metal layer extends in the vicinity of or along exclusively an edge of a coupling area extending substantially in parallel to a lineup direction of the terminals and/or an edge of the coupling area extending substantially at a right angle with the lineup direction, the coupling area being for making the conductive layer to be exposed to the exterior in an area of the terminal of the conductive layer; and there is provided an electrically insulating layer which covers at least a part of the terminal of the conductive layer and at least a main portion of the metal layer and which extends on the area other than the coupling area in an area of the

[3]

3. An electronic apparatus as defined in Claim 1 or 2, wherein the conductive layer is connected to a terminal of peripheral circuitry via an anisotropic conductive film in the coupling area.

terminal of the conductive layer.

[4]

4. An electronic apparatus as defined in Claim 1, wherein the metal layer is formed surrounding the coupling area in an area of the terminal of the conductive layer.

[5]

5. An electronic apparatus as defined in any one of Claims 1-4, wherein the coupling area is formed in a shape including at least one linear edge on its plan

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view, and the metal layer is formed along the linear edge in an area of the terminal of the conductive layer.

- [6] 6. An electronic apparatus as defined in any one of Claims 1-5, wherein the insulating layer includes a first insulating layer which has been patterned together with the metal layer and is stacked on the metal layer, and a second insulating layer which covers at least a part of the first insulating layer having been patterned and a side of the metal layer.
- 7. An electronic apparatus as defined in any one of Claims 1-6, wherein the conductive layer, the metal layer and/or the insulating layer are/is in the same layer as in those/that of a conductive film, a metal film and/or an insulating film, respectively, used for a display element or driving device formed in the electronic apparatus.